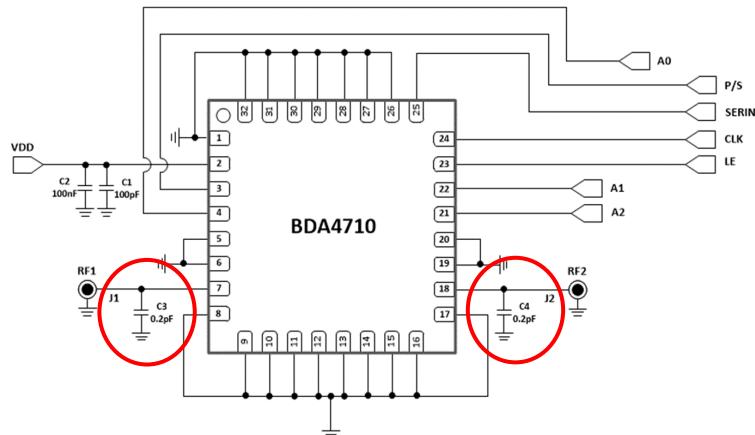


BDA4710/BDA4730 SWR Improvement Matching Circuits for over 4GHz

BeRex offers SWR(S11, S22) improvement circuits on the BDA4710/BDA4730. This application note explains the method to improve Input / output Return loss of 4GHz and above. It will be helpful when using a frequency band of 4GHz or higher. For more detailed product specifications, please check the datasheet.

Application Schematic


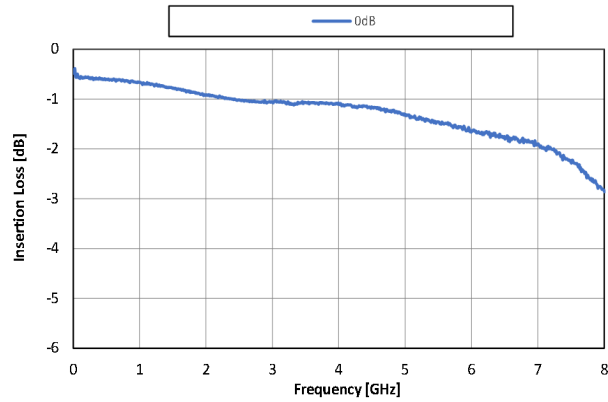
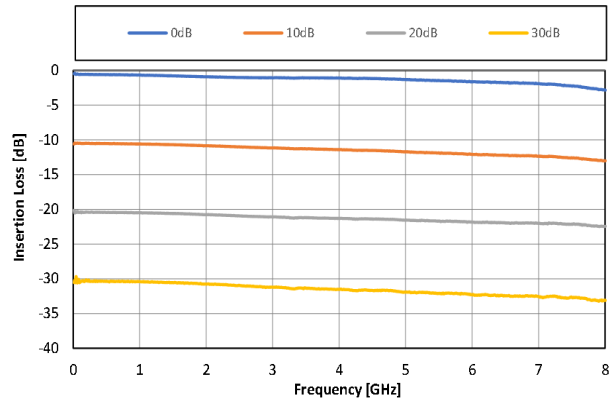
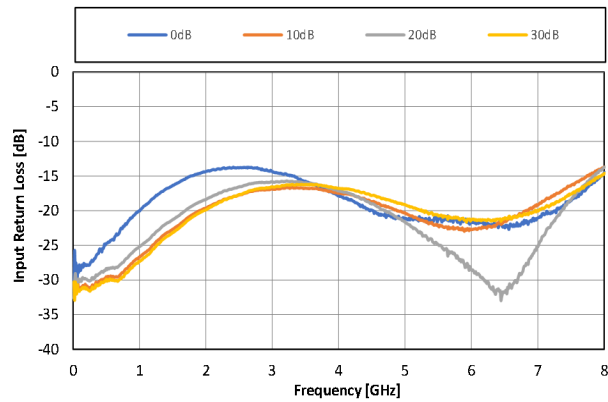
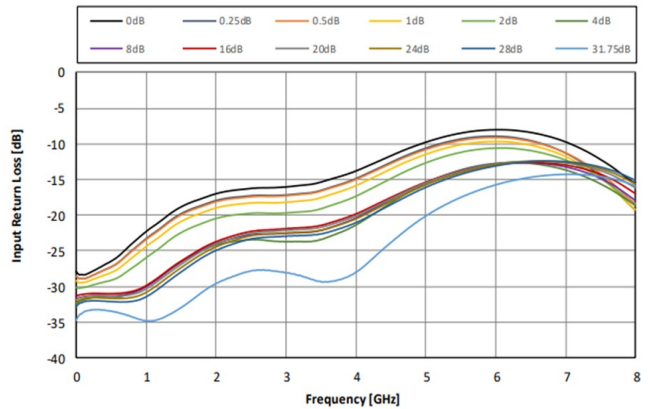
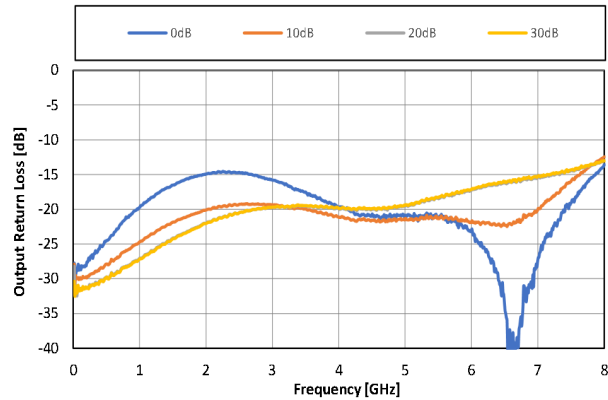
Reference	Part	Value	Remark
C1	Capacitor	100pF	
C2	Capacitor	100nF	
C3	Capacitor	0.2pF	Matching Value
C4	Capacitor	0.2pF	Matching Value

Figure 1. BDA4710 / BDA4730 Application Circuits for SWR improvement of Frequency 4GHz to 8GHz.

Test Results

When 0.2pF of shunt capacitor is added to RF1 and RF2 port, respectively, both S11 and S22 are improved to below -15dB in frequency band 4GHz or higher. Table 4 below shows the BDA4710 / BDA4730 S parameter results when shunt 0.2pF is added on BVA4710 / BDA4730 EVB.

			4GHz	4.5GHz	5GHz	5.5GHz	6GHz	6.5GHz	7GHz	7.5GHz	8GHz	Remark
S Parameter	Insertion Loss S21 [dB]	ATT=0dB	-1.08	-1.16	-1.33	-1.46	-1.66	-1.75	-1.92	-2.24	-2.83	Figure 2
		ATT=10dB	-11.38	-11.51	-11.73	-11.86	-12.08	-12.20	-12.35	-12.57	-13.02	Figure 2,3
		ATT=20dB	-21.28	-21.39	-21.58	-21.68	-21.82	-21.94	-22.02	-22.13	-22.47	Figure 3
		ATT=30dB	-31.51	-31.66	-31.94	-32.09	-32.27	-32.46	-32.51	-32.61	-33.06	Figure 3
		Flatness (ATT=0dB)	0.6dBpp						1.2dBpp			
	Input Return Loss S11 [dB]	ATT=0dB	-17.87	-19.92	-21.20	-21.40	-21.51	-22.23	-21.17	-18.60	-14.62	Figure 4
		ATT=10dB	-17.48	-18.71	-20.25	-22.19	-22.74	-21.40	-19.11	-16.43	-13.75	Figure 4
		ATT=20dB	-17.17	-19.05	-21.56	-24.89	-28.45	-31.80	-25.17	-18.53	-13.78	Figure 4
		ATT=30dB	-16.79	-17.82	-19.08	-20.63	-21.32	-21.13	-19.97	-17.61	-14.71	Figure 4
	Output Return Loss S22 [dB]	ATT=0dB	-19.69	-20.87	-21.22	-21.21	-23.22	-33.42	-27.54	-19.09	-13.63	Figure 5
		ATT=10dB	-21.12	-21.58	-21.55	-21.19	-21.91	-22.25	-20.14	-16.06	-12.49	Figure 5
		ATT=20dB	-19.92	-20.02	-19.61	-18.37	-17.29	-16.23	-15.45	-14.45	-13.06	Figure 5
ATT=30dB		-19.84	-19.92	-19.46	-18.30	-17.17	-16.11	-15.26	-14.34	-13.01	Figure 5	
ATT Accuracy	ATT Error	ATT=10dB	0.30	0.36	0.40	0.40	0.42	0.45	0.44	0.33	0.18	
		ATT=20dB	0.21	0.23	0.25	0.23	0.15	0.19	0.10	-0.11	-0.36	
		ATT=30dB	0.43	0.50	0.61	0.63	0.61	0.71	0.59	0.37	0.23	
	ATT Accuracy		±(0.25 +5.0% of attenuation state)					±(0.25+7.0% of attenuation state)				

BDA4710/BDA4730 SWR Improvement Matching Circuits for over 4GHz
Test Results (Graph)
Figure 2. S21 (ATT = 0dB) : Added 0.2pF Shunt

Figure 3. S21 (ATT = 0dB, 10dB, 20dB, 30dB) : Added 0.2pF Shunt

Figure 4. S11 (ATT = 0, 10, 20, 30dB) : Added 0.2pF Shunt.

Figure 4-1. S11 : No matching, datasheet graph

Figure 5. S22 (ATT = 0, 10, 20, 30dB) : Added 0.2pF Shunt.

Figure 5-1. S22 : No matching state, datasheet graph
