

RF MMIC Innovator

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[CLASSIFICATION] APPLICATION NOTE

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[REVISION No.] REV.1.1

[MEASURING INSTRUMENTS]

- NA_AGILENT E5071B

- SA_AGILENT N9020A

- SG_AGILENT 4438C

- SG_AGILENT N5182A

High Power Amp BMT321

Application Note



Contents

RF MMIC INNOVATOR	WWW.BEREX.COM	1
1. 1805~1880MHZ APPLICATION		3
1.1 1805~1880MHZ TEST RESULT(S-PARAMETER, OIP3, P1, NF)		4
1.2 1805~1880MHZ TEST RESULT(WCDMA_1FA_ACLR)		5
1.3 1805~1880MHZ TEST RESULT(LTE10MHZ_ACLR)		6
1.4 1805~1880MHZ TEST RESULT(LTE20MHZ_ACLR)		7
2. 1930~1990MHZ APPLICATION		8
2.1 1930~1990MHZ TEST RESULT(S-PARAMETER, OIP3, P1, NF)		9
2.2 1930~1990MHZ TEST RESULT(WCDMA_1FA_ACLR)		10
2.3 1930~1990MHZ TEST RESULT(LTE10MHZ_ACLR)		11
2.4 1930~1990MHZ TEST RESULT(LTE20MHZ_ACLR)		12
3. 2110~2170MHZ APPLICATION		13
3.1 2110~2170MHZ TEST RESULT(S-PARAMETER, OIP3, P1, NF)		14
3.2 2110~2170MHZ TEST RESULT(WCDMA_1FA_ACLR)		15
3.3 2110~2170MHZ TEST RESULT(LTE10MHZ_ACLR)		16
3.4 2110~2170MHZ TEST RESULT(LTE20MHZ_ACLR)		17
4. 2620~2690MHZ APPLICATION		18
4.1 2620~2690MHZ TEST RESULT(S-PARAMETER, OIP3, P1, NF)		19
4.2 2620~2690MHZ TEST RESULT(WCDMA_1FA_ACLR)		20
4.3 2620~2690MHZ TEST RESULT(LTE10MHZ_ACLR)		21
4.4 2620~2690MHZ TEST RESULT(LTE20MHZ_ACLR)		22
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1. BMT321 _ 1805~1880MHz Application Note

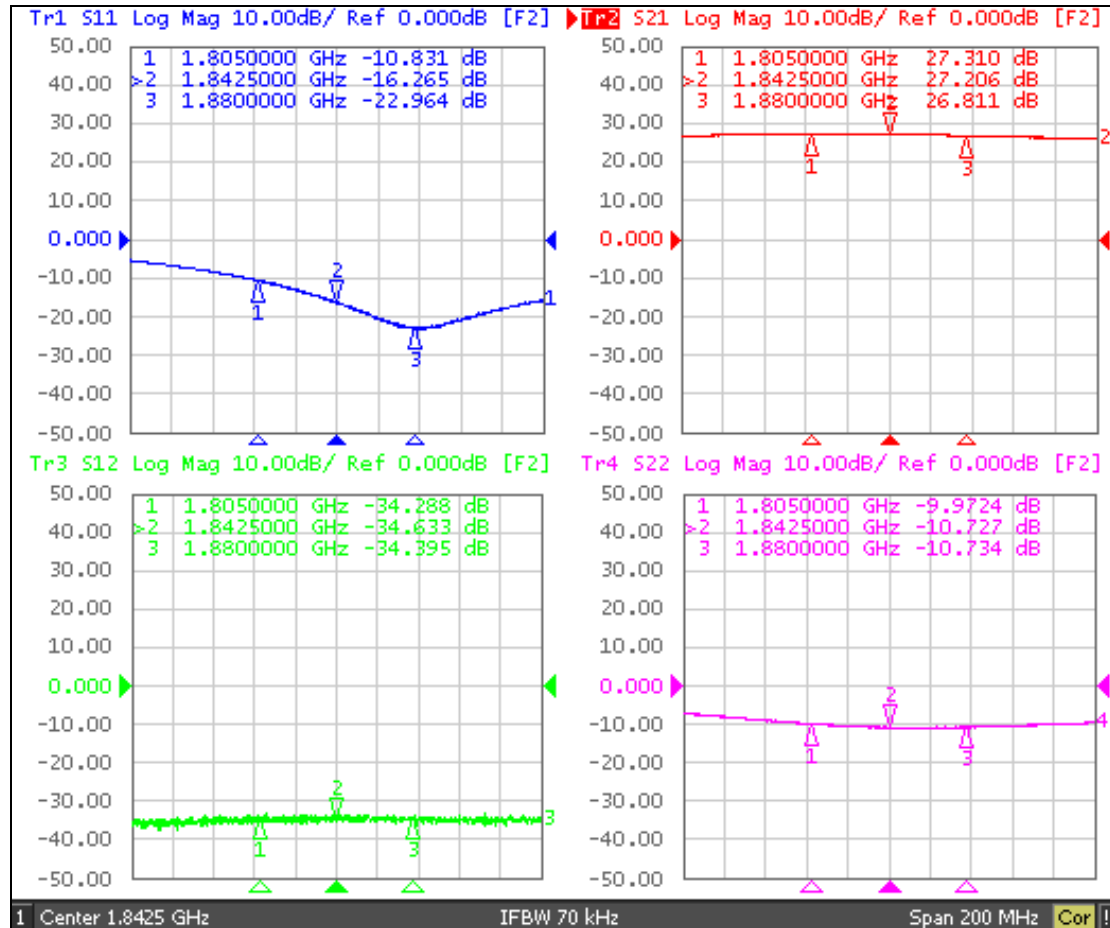
Schematic Diagram	BOM			Marks
	C1	0603	1uF	
	C2	0603	1uF	
	C3	0603	22pF	
	C4	0603	22pF	
	C5	0603	N/A	
	C6	0603	N/A	
	C7	0603	5pF	
	C8	0603	22pF	
	C9	0603	N/A	
	C10	0603	2pF	
	C11	0603	N/A	
	C12	0603	N/A	
	C13	0603	N/A	
	C14	0603	1uF	
	C15	0603	22pF	
	C16	0603	1.2pF	
	C17	0603	2.7pF	
	L1	0603	18nH	
L2	1008	22nH	Coil	
L3	0603	22nH		
R1	0603	300 Ω	±5%	
R2	0603	420 Ω	±5%	
R3	0603	82 Ω	±5%	

PCB Diagram	Notice		
	Below information is subject to change as conditions of the substrate.		
	Reference	Object	Distance
	Input pin	L3	7.9mm
	Input pin	C16	3.5mm
	Output pin	C17	3.9mm
Pin 5	C10	3.1mm	
<p>1. We recommend to adjust capacitance of C10, when balance for multicarrier response of LTE20MHz is broken .</p>			

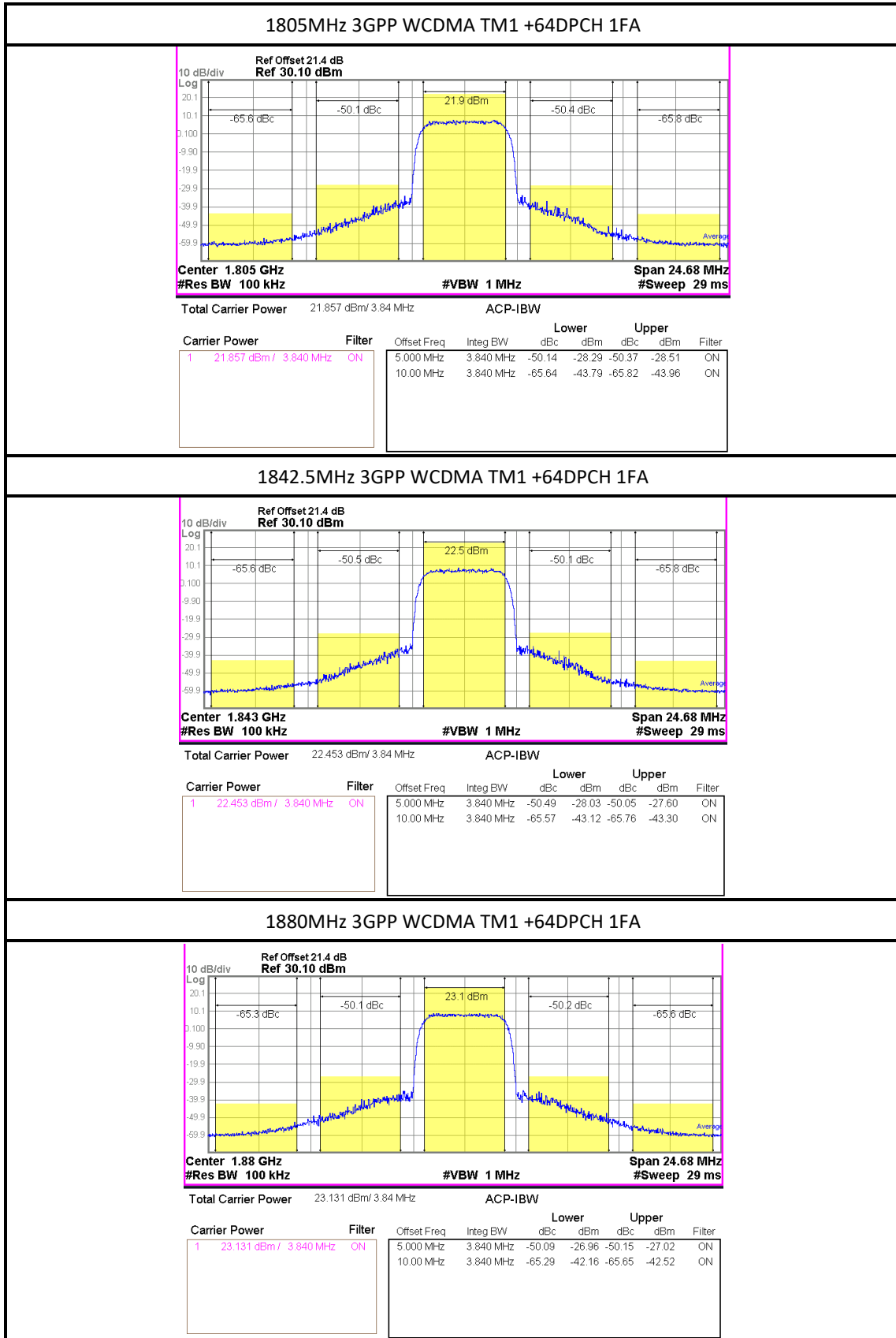
1.1 BMT321 _ 1805~1880MHz Test Result

Freq [MHz]	Vcc [V]	Iref [mA]	Icq [mA]	Gain [dB]	OIP3 [dBm] ⁽¹⁾	P1dB [dBm]	IRL [dB]	ORL [dB]	NF [dB]
1805	5	26	359	27.3	43.8	31.0	-10.8	-9.9	-
1842.5	5	26	359	27.2	43.8	31.2	-16.2	-10.7	-
1880	5	26	359	26.8	43.5	32.0	-22.9	-10.7	-

(1) OIP3 was tested @Pout=17dBm/tone (CW) 1MHz offset



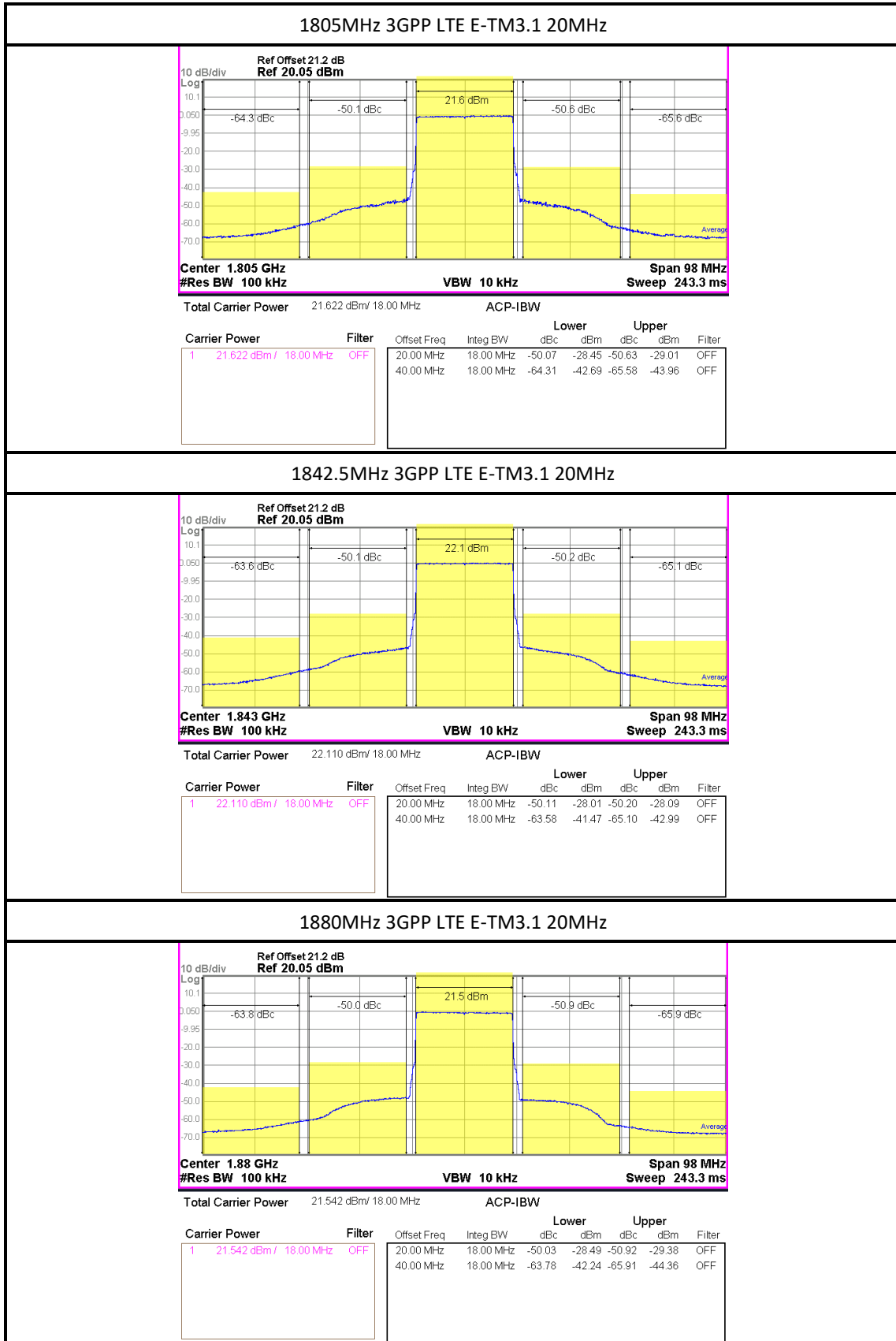
1.2 BMT321_1805~1880MHz WCDMA 1FA ACLR Test Result



1.3 BMT321_1805~1880MHz LTE10MHz ACLR Test Result



1.4 BMT321_1805~1880MHz LTE20MHz ACLR Test Result



2. BMT321_1930~1990MHz Application Note

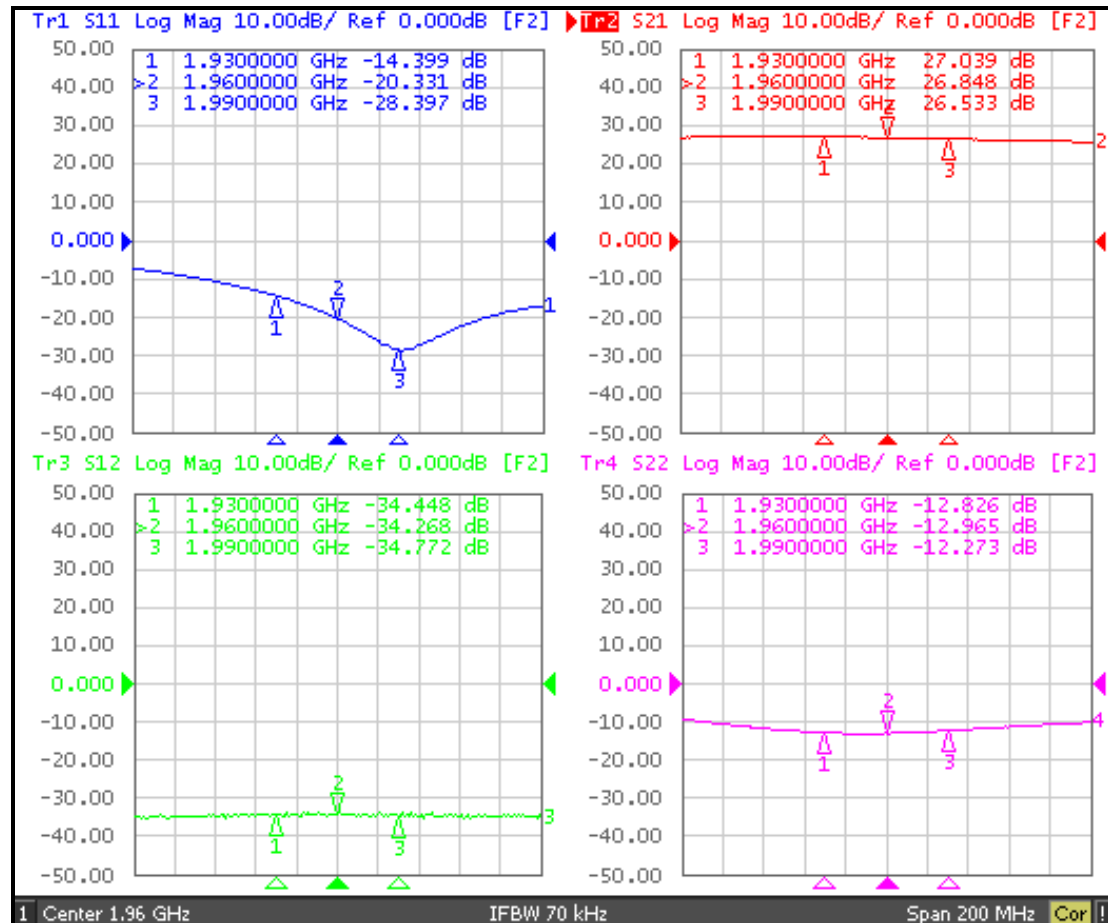
Schematic Diagram	BOM			Marks
	C1	0603	1uF	
	C2	0603	1uF	
	C3	0603	22pF	
	C4	0603	22pF	
	C5	0603	N/A	
	C6	0603	N/A	
	C7	0603	5pF	
	C8	0603	22pF	
	C9	0603	N/A	
	C10	0603	1.5pF	
	C11	0603	N/A	
	C12	0603	N/A	
	C13	0603	N/A	
	C14	0603	1uF	
	C15	0603	22pF	
C16	0603	1.2pF		
C17	0603	2.5pF		
L1	0603	12nH		
L2	1008	22nH	Coil	
L3	0603	22nH		
R1	0603	300 Ω	±5%	
R2	0603	420 Ω	±5%	
R3	0603	82 Ω	±5%	

PCB Diagram	Notice		
	Below information is subject to change as conditions of the substrate.		
	Reference	Object	Distance
	Input pin	L3	6.8mm
	Input pin	C16	3.5mm
	Output pin	C17	3.5mm
Pin 5	C10	3.1mm	
<p>1930~1990GHz Application Circuit QFN3x3_12L PKG Type 1.5W 2Stage_0.4T Ver3.0 PCB=FR-4, Oz=1.0 Er=4.6, Line=0.588mm, Clearance=1mm, HEIGHT=0.4T BEREX_151105</p>	<p>1. We recommend to adjust capacitance of C10, when balance for multicarrier response of LTE20MHz is broken .</p>		

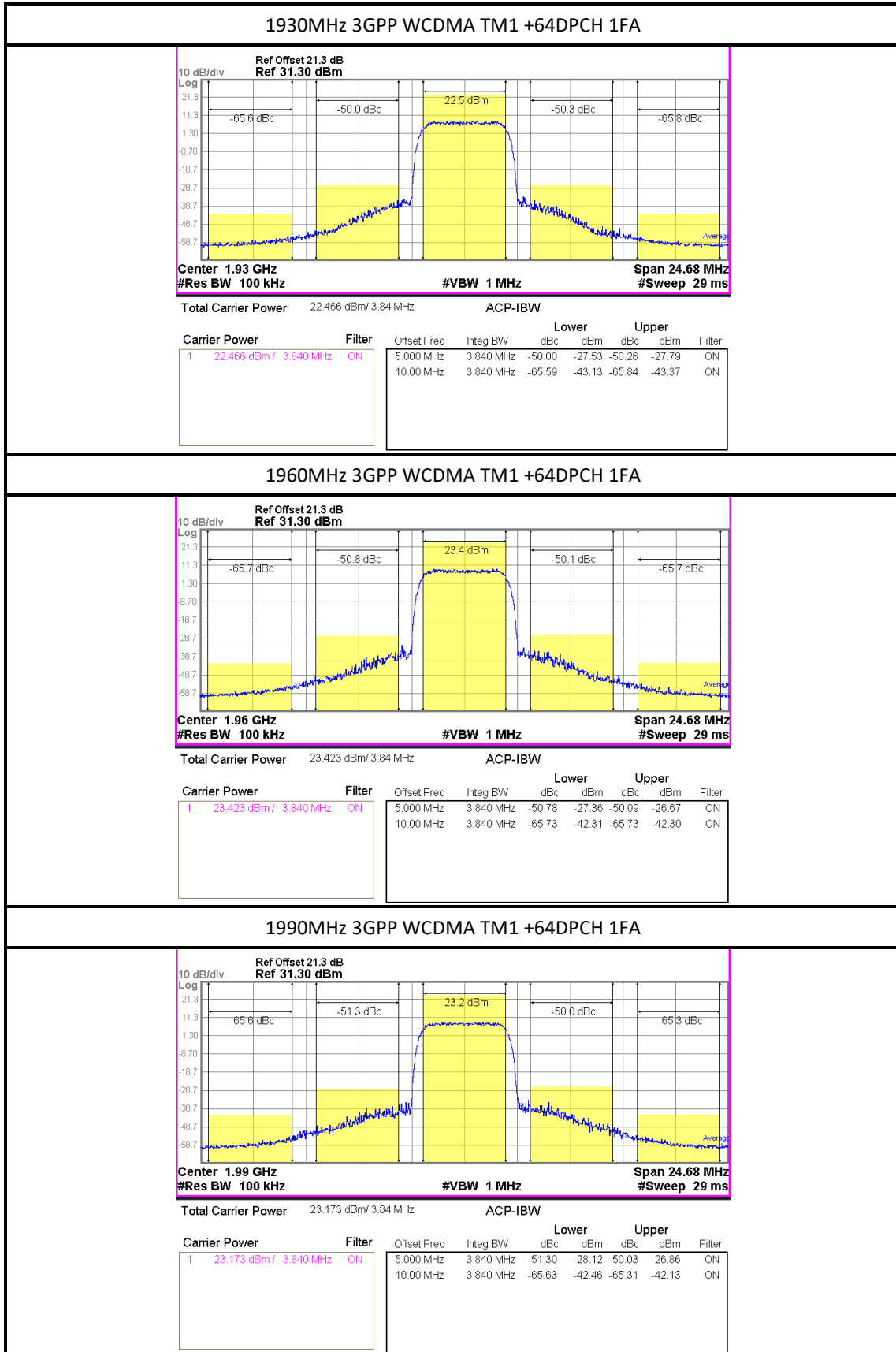
2.1 BMT321_1930~1990MHz Test Result

Freq [MHz]	Vcc [V]	Iref [mA]	Icq [mA]	Gain [dB]	OIP3 [dBm] ⁽¹⁾	P1dB [dBm]	IRL [dB]	ORL [dB]	NF [dB]
1930	5	26	355	27.0	43.0	31.2	-14.4	-12.8	-
1960	5	26	355	26.8	43.7	32.0	-20.3	-12.9	-
1990	5	26	355	26.5	46.4	32.1	-28.4	-12.2	-

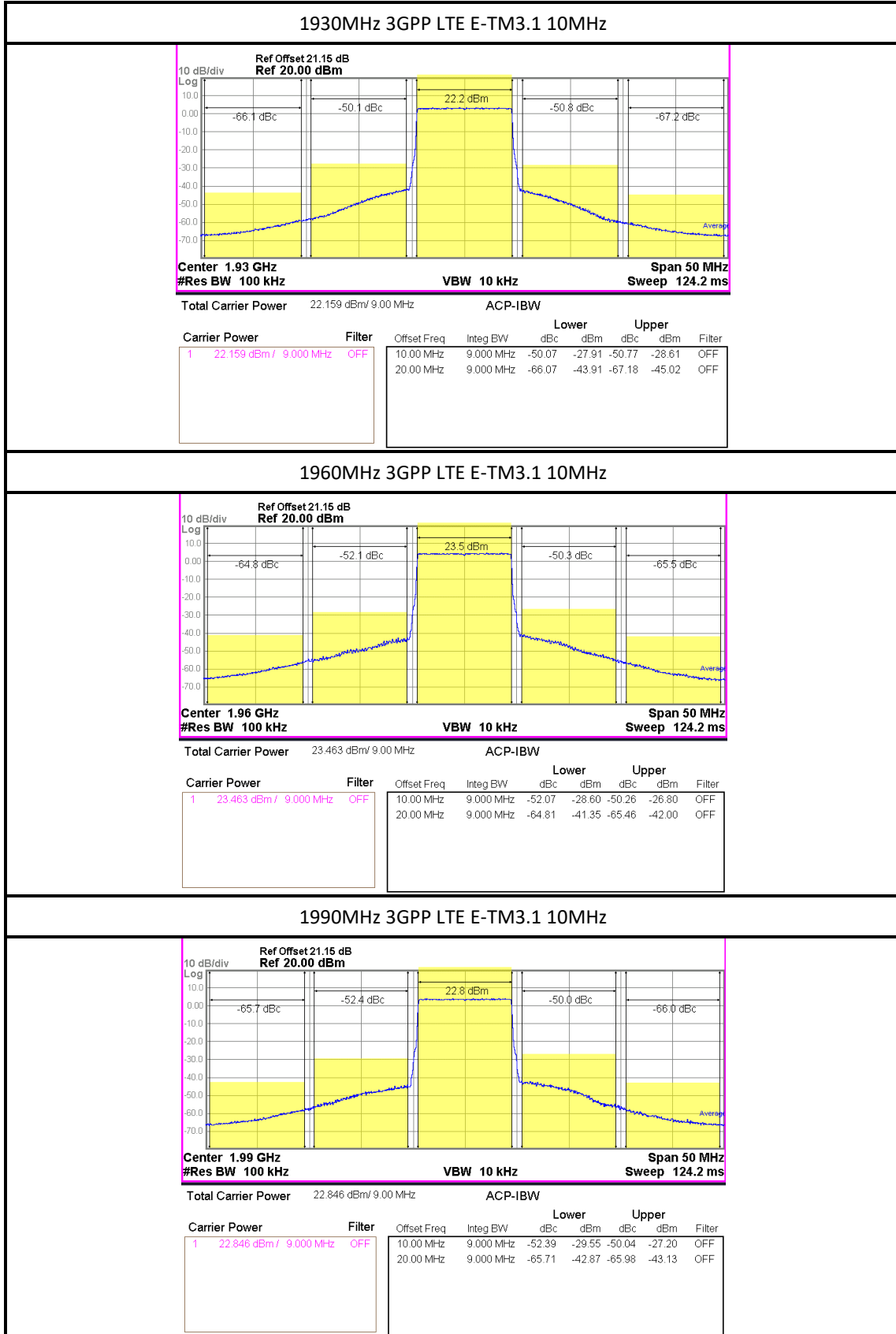
(1) OIP3 was tested @Pout=17dBm/tone (CW) 1MHz offset



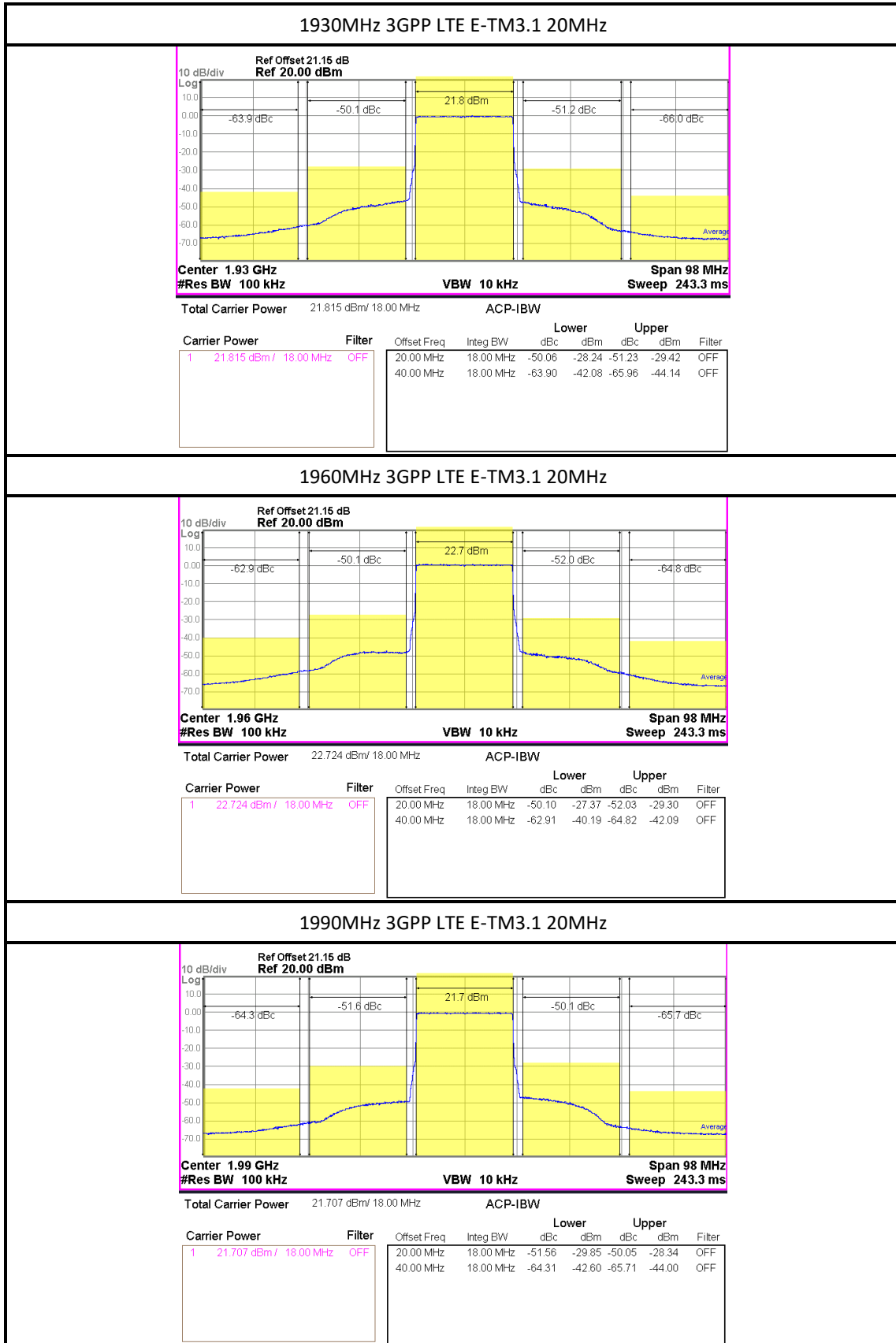
2.2 BMT321_1930~1990MHz WCDMA 1FA ACLR Test Result



2.3 BMT321_1930~1990MHz LTE10MHz ACLR Test Result



2.4 BMT321_1930~1990MHz LTE20MHz ACLR Test Result



3. BMT321 2110~2170MHz Application Note

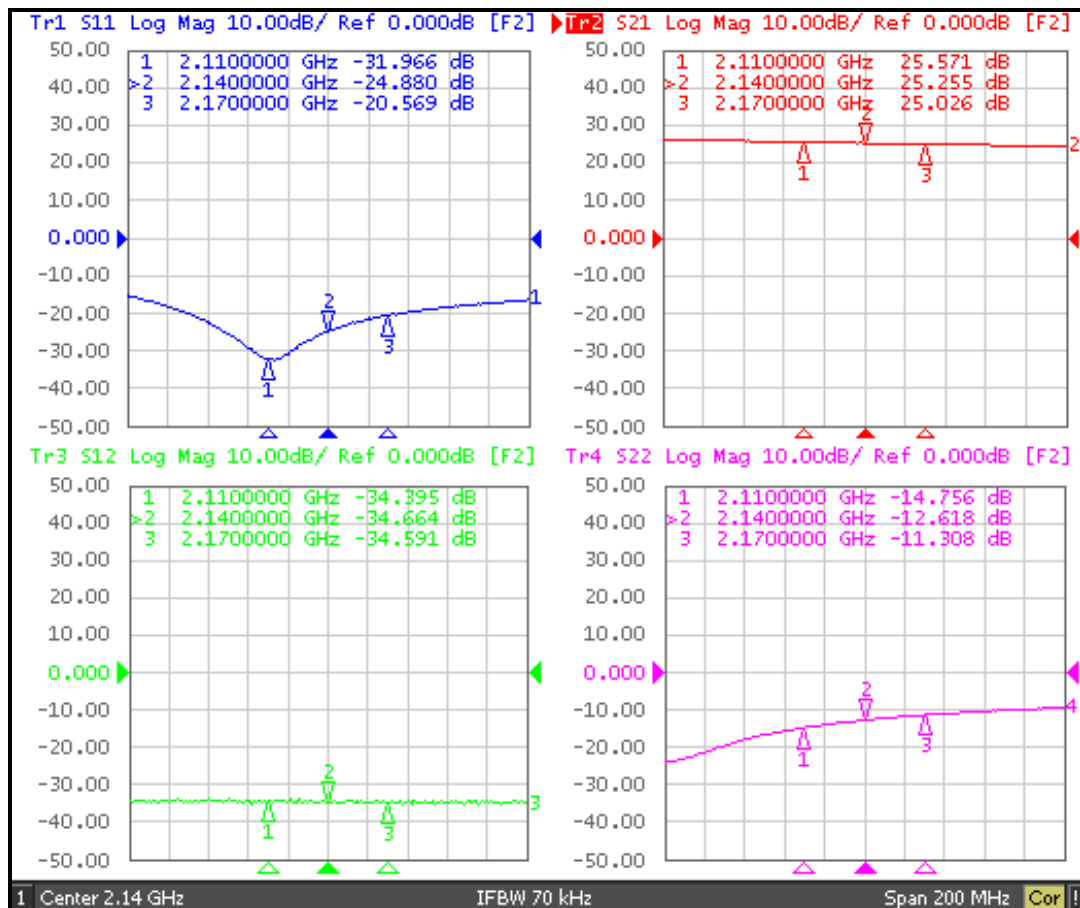
Schematic Diagram	BOM			Marks
	C1	0603	1uF	
	C2	0603	1uF	
	C3	0603	22pF	
	C4	0603	22pF	
	C5	0603	N/A	
	C6	0603	N/A	
	C7	0603	5pF	
	C8	0603	22pF	
	C9	0603	N/A	
	C10	0603	1.2pF	
	C11	0603	N/A	
	C12	0603	N/A	
	C13	0603	N/A	
	C14	0603	1uF	
	C15	0603	22pF	
	C16	0603	1.2pF	
C17	0603	2.2pF		
L1	0603	33nH		
L2	1008	18nH	Coil	
L3	0603	2.2nH		
R1	0603	300 Ω	±5%	
R2	0603	420 Ω	±5%	
R3	0603	82 Ω	±5%	

PCB Diagram	Notice		
<p>2110~2170MHz Application Circuit OFN3x3_12L PKG Type 1.5W 25Tape_0.4T Ver.3.0 PCB=FR-4, Oz=1.0 Er=4.6, Line=0.586mm, Clearance=1mm, HEIGHT=0.4T BEREX_151105</p>	Below information is subject to change as conditions of the substrate.		
	Reference	Object	Distance
	Input pin	L3	5.5mm
	Input pin	C16	3.5mm
Output pin	C17	3.4mm	
Pin 5	C10	3.1mm	
<p>1. We recommend to adjust capacitance of C10, when balance for multicarrier response of LTE20MHz is broken .</p>			

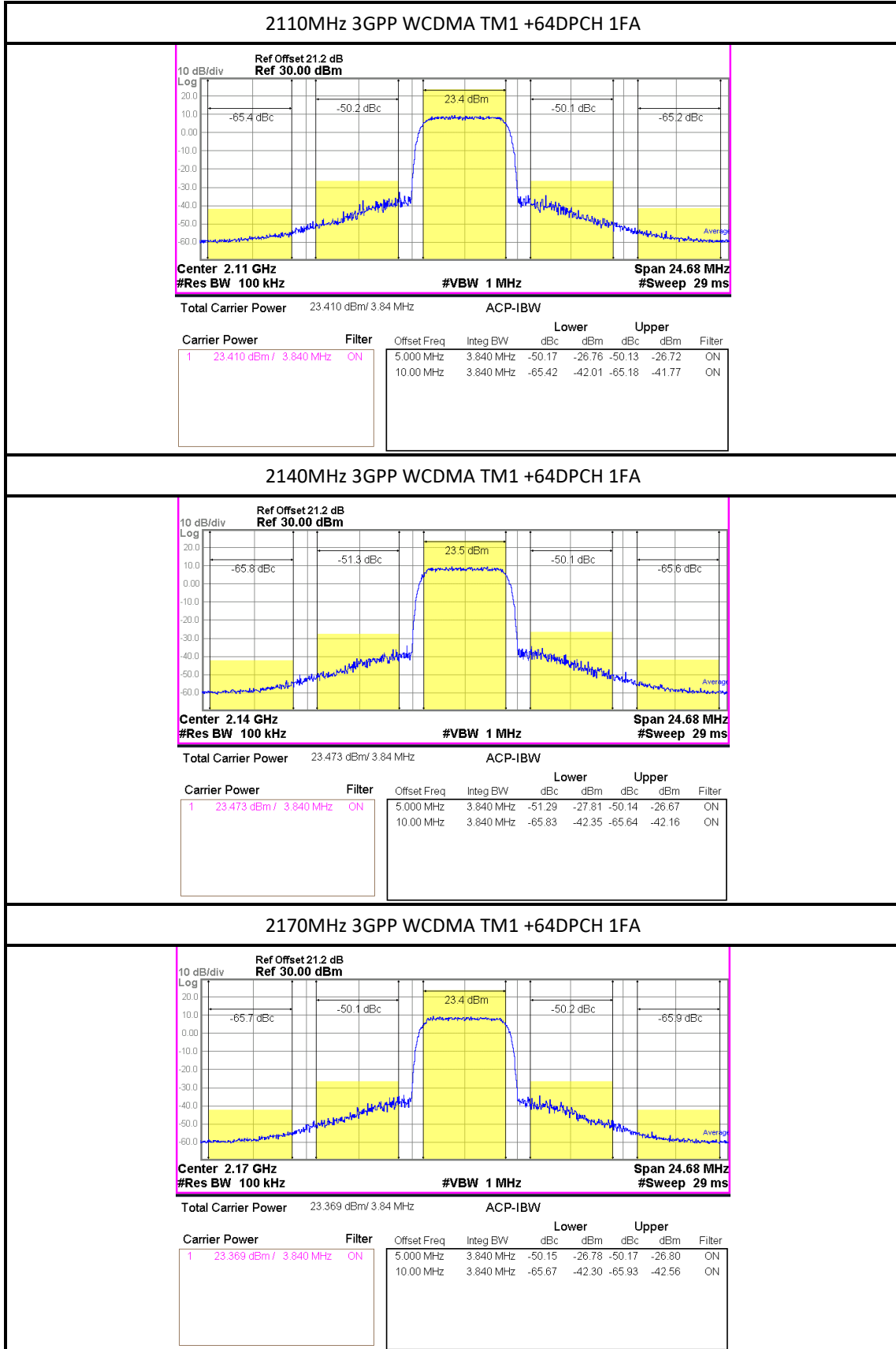
3.1 BMT321 2110~2170MHz Test Result

Freq [MHz]	Vcc [V]	Iref [mA]	Icq [mA]	Gain [dB]	OIP3 [dBm] ⁽¹⁾	P1dB [dBm]	IRL [dB]	ORL [dB]	NF [dB]
2110	5	26	357	25.5	45.0	31.7	-31.9	-14.7	-
2140	5	26	357	25.2	48.6	32.2	-24.8	-12.6	-
2170	5	26	357	25.0	48.6	32.0	-20.5	-11.3	-

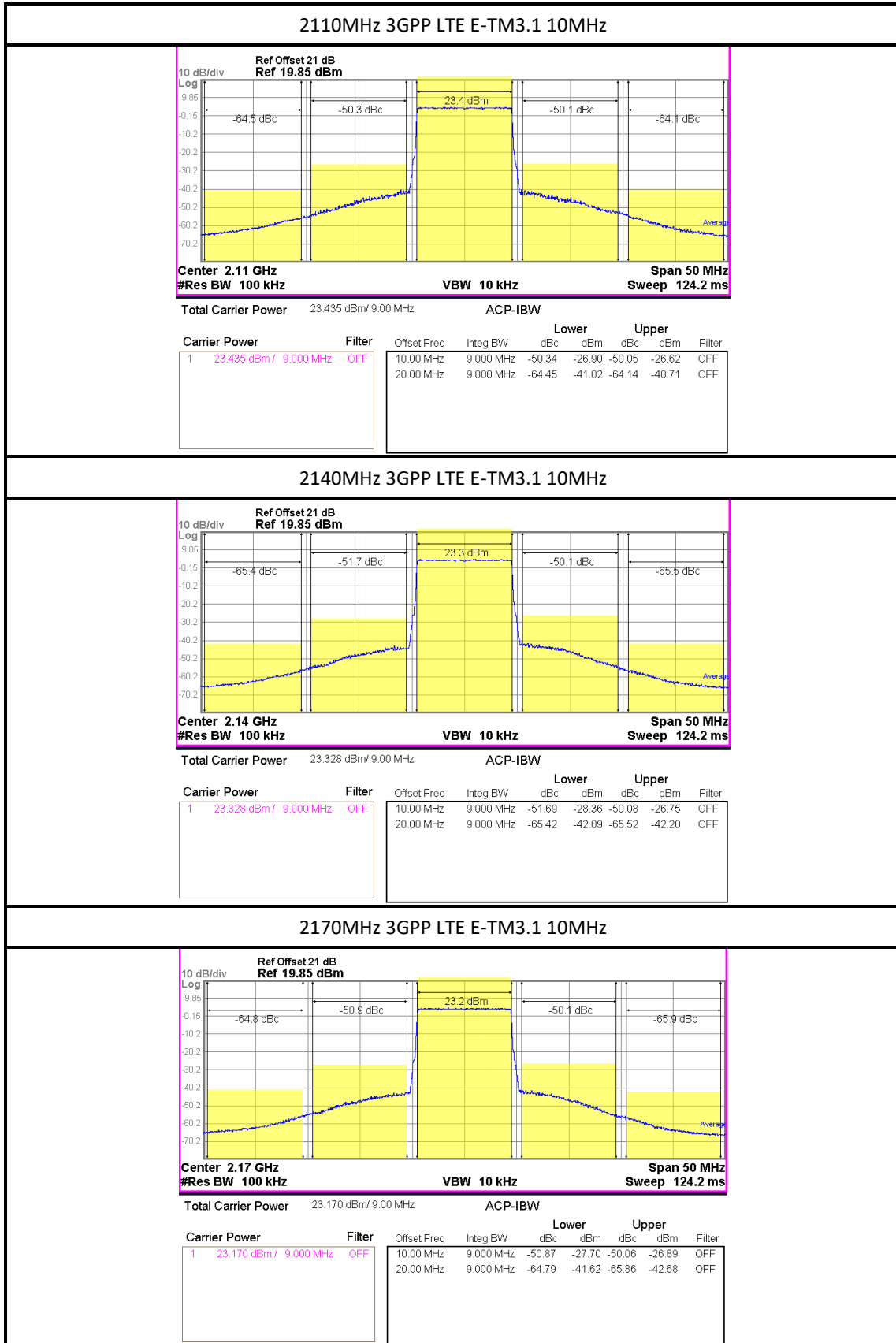
(1) OIP3 was tested @Pout=17dBm/tone (CW) 1MHz offset



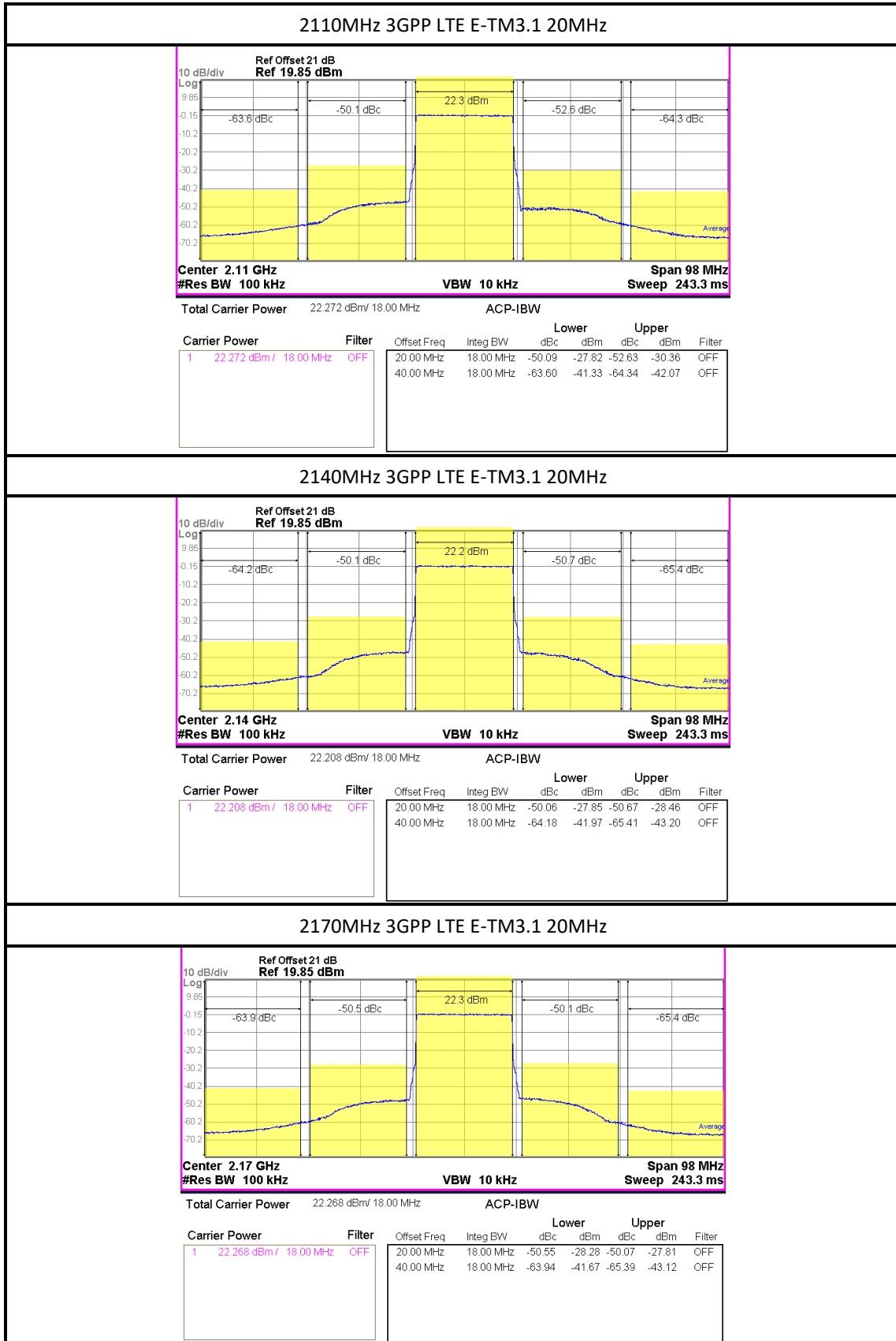
3.2 BMT321_2110~2170MHz WCDMA 1FA ACLR Test Result



3.3 BMT321_2110~2170MHz LTE10MHz ACLR Test Result



3.4 BMT321_2110~2170MHz LTE20MHz ACLR Test Result



4. BMT321 2620~2690MHz Application Note

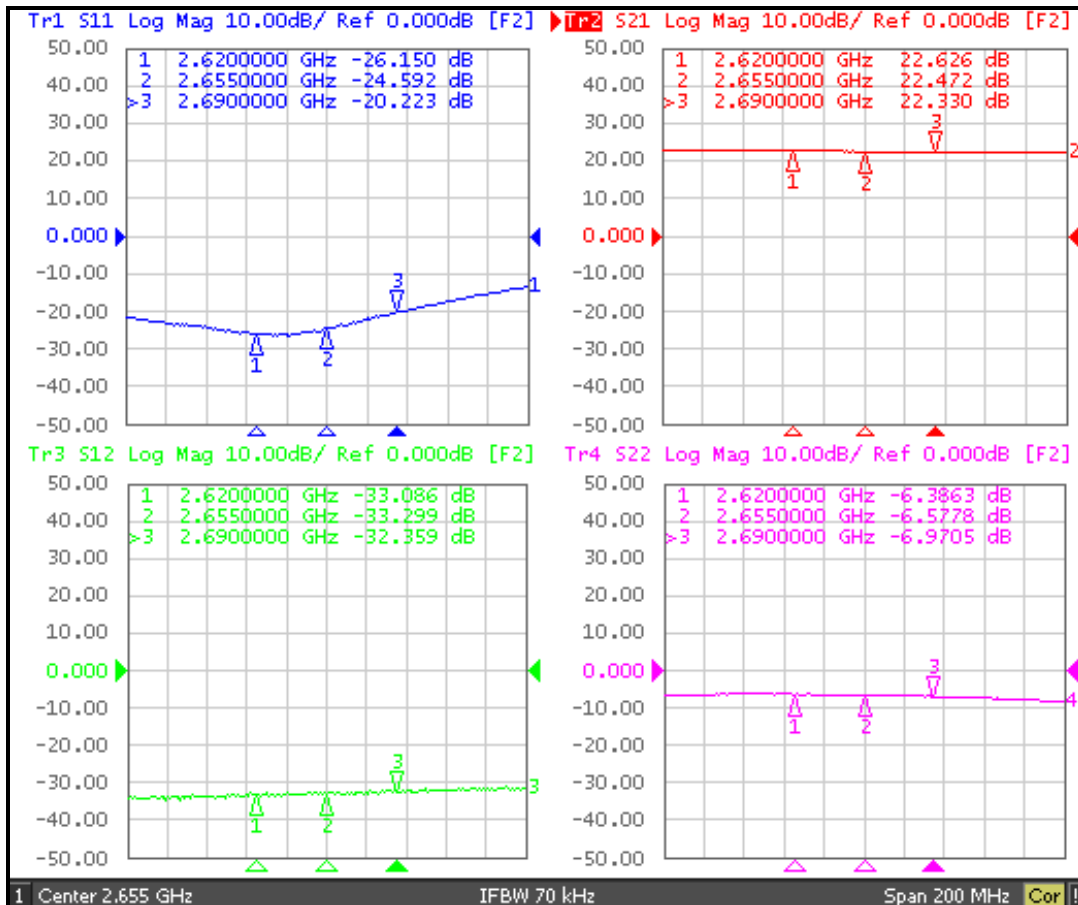
Schematic Diagram	BOM	Marks		
	C1	0603	1uF	
	C2	0603	1uF	
	C3	0603	22pF	
	C4	0603	22pF	
	C5	0603	N/A	
	C6	0603	N/A	
	C7	0603	5pF	
	C8	0603	22pF	
	C9	0603	N/A	
	C10	0603	N/A	
	C11	0603	N/A	
	C12	0603	N/A	
	C13	0603	N/A	
	C14	0603	1uF	
	C15	0603	22pF	
	C16	0603	0.75pF	
C17	0603	1.5pF		
L1	0603	27nH		
L2	1008	18nH	Coil	
L3	0603	1.8nH		
R1	0603	300 Ω	±5%	
R2	0603	420 Ω	±5%	
R3	0603	82 Ω	±5%	

PCB Diagram	Notice		
	Below information is subject to change as conditions of the substrate.		
	Reference	Object	Distance
	Input pin	L3	7.1mm
	Input pin	C16	3.5mm
	Output pin	C17	3.5mm
Pin 5	C10	2.7mm	

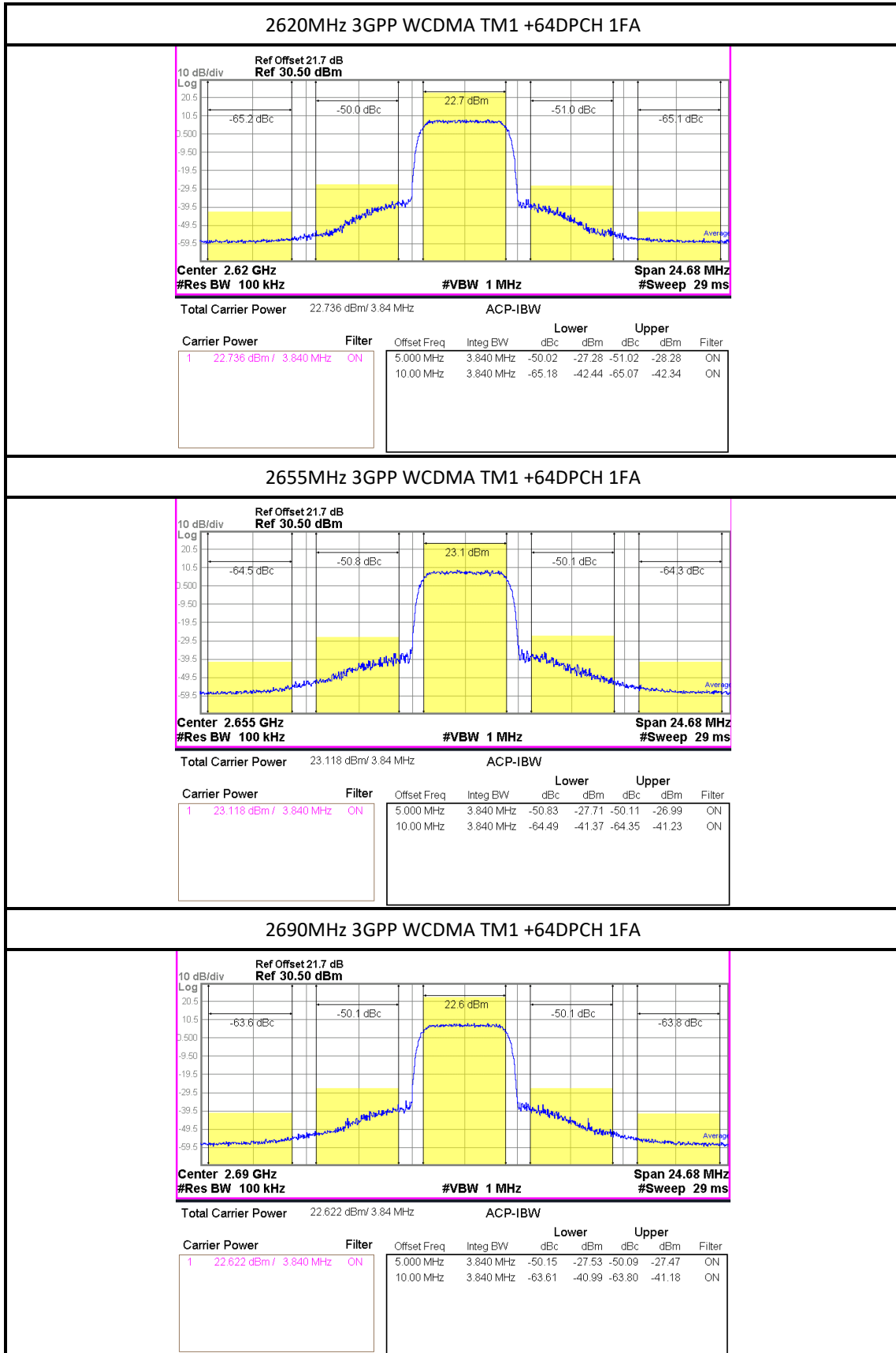
4.1 BMT321 2620~2690MHz Test Result

Freq [MHz]	Vcc [V]	Iref [mA]	Icq [mA]	Gain [dB]	OIP3 [dBm] ⁽¹⁾	P1dB [dBm]	IRL [dB]	ORL [dB]	NF [dB]
2620	5	26	360	22.6	45.1	31.3	-26.1	-6.3	-
2655	5	26	360	22.4	46.3	31.5	-24.5	-6.5	-
2690	5	26	360	22.3	42.6	31.0	-20.2	-6.9	-

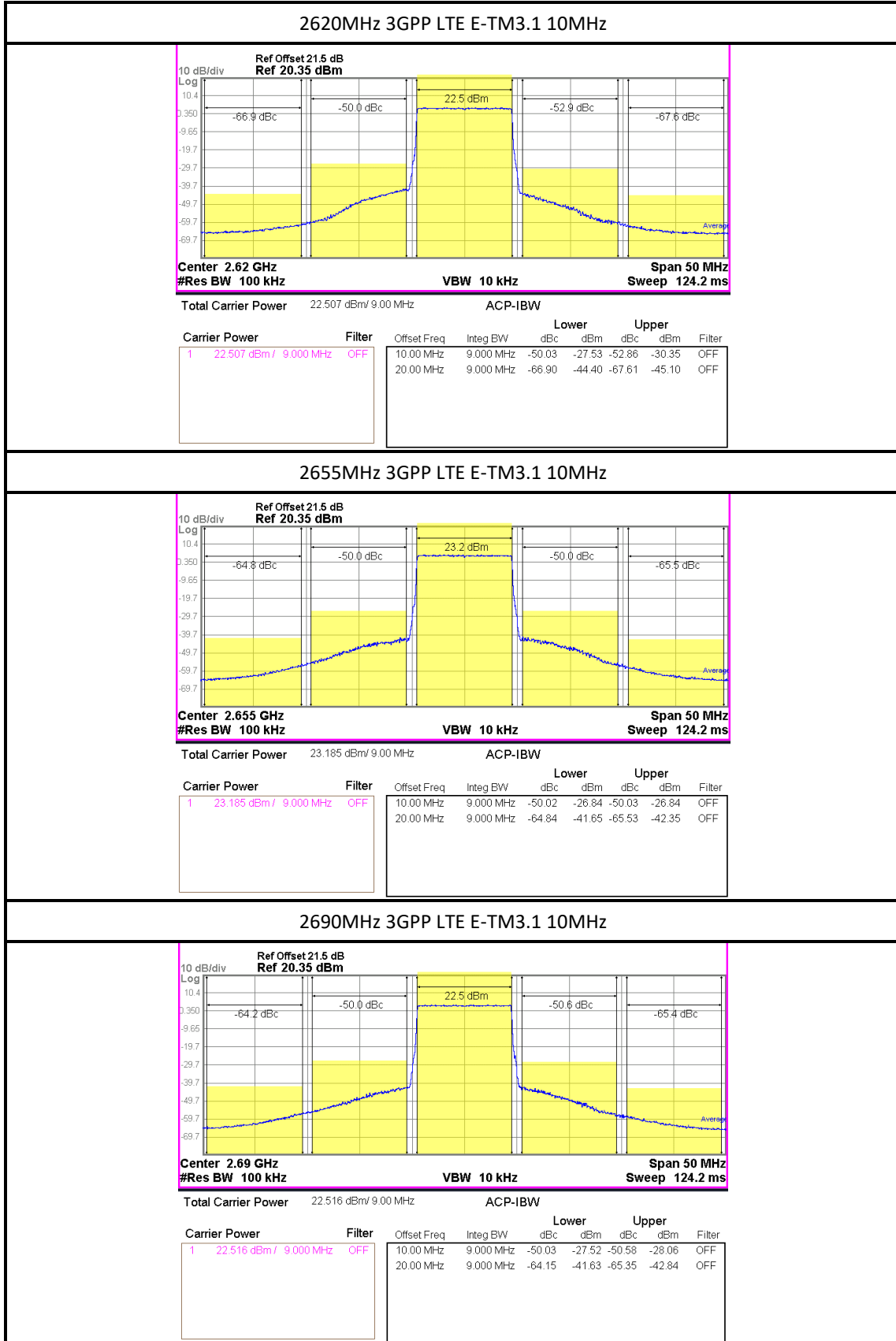
(1) OIP3 was tested @Pout=17dBm/tone (CW) 1MHz offset



4.2 BMT321_2620~2690MHz WCDMA 1FA ACLR Test Result



4.3 BMT321_2620~2690MHz LTE10MHz ACLR Test Result



1.1 BMT321_2620~2690MHz LTE20MHz ACLR Test Result

