# **BCG008**



# 8W GaN Power Transistor

## 8W GaN Power Transistor (0.15µm x 1250µm gate)

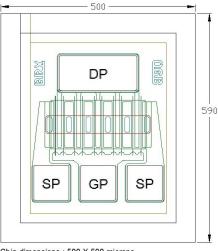
The BeRex BCG008 is a GaN Power HEMT die with a nominal 0.15 micron gate length and 1250 micron gate width making the product ideally suited for amplifier applications where high-gain and high power from DC to 26 GHz. The product may be used in either wide-band or narrow-band applications. The BCG008 is produced using state of the art metallization with SI3N4 passivation and is screened to assure reliability.

#### **Product Features**

- 39.0 dBm Typical Saturated Output Power (P3dB) @ 12 GHz
- 9.5 dB Typical Saturated gain (G3dB) @ 12 GHz
- 72 % PAE Typical @ 12 GHz
- 0.15 X 1250 Micron Recessed Gate

#### Applications

- Commercial
- Military / Hi-Rel.
- Test & Measurement



Chip dimensions : 500 X 590 microns Gate pad(GP) : 90 X 90 microns Drain pad(DP) : 200 X 90 microns Source pad(SP) : 90 X 90 microns Chip thickness : 75 microns

#### **Typical Performance**

SYMBOLS	PARAMETER/TEST CONDITIONS	TEST FRE- QUENCY	MIN.	TYPICAL	Max	UNIT
P3dB	Saturated Output Power @ P3dB (Vds = 28V, Id = 60mA) 12 GHz		37.5	39.0		dBm
G3dB	Power Gain @ P3dB (Vds = 28V, Id = 60mA)	12 GHz	8.0	9.5		dB
PAE	PAE @ P3dB (Vds = 28V, Id = 60mA)	12 GHz		72		%
l <sub>dss</sub>	Saturated Drain Current ( $V_{gs} = 0.0 V$ , $V_{ds} = 10.0 V$ )			640	770	mA
V <sub>p</sub>	Pinch-off Voltage ( $I_{ds}$ = 1.25 mA, $V_{ds}$ = 10 V)			-1.9		V
$BV_{gd}$	Drain Breakdown Voltage (I <sub>g</sub> = 1.25 mA, source open)			84		V
BV <sub>gs</sub>	Source Breakdown Voltage (I <sub>g</sub> = 1.25 mA, drain open)			-6.5		V
R <sub>th</sub>	Thermal Resistance			4.9		° C/W

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## MAXIMUM RATING (Ta = 25° C)

SYMBOLS	PARAMETERS	ABSOLUTE
V <sub>ds</sub>	Drain-Source Voltage	90 V
$V_{gs}$	Gate-Source Voltage	-10 V
I <sub>ds</sub>	Drain Current	1.5 A
$I_{gsf}$	Forward Gate Current	2 mA
$T_{stg}$	Storage Temperature	-60° C to 150° C
Pt	Total Power Dissipation	15.0 W

Exceeding any of the above Maximum Ratings will result in reduced MTTF and may cause permanent damage to the device.

FREQ.	S11	S11	S21	S21	S12	S12	S22	S22
[GHZ]	[MAG]	[ANG.]	[MAG]	[ANG.]	[MAG]	[ANG.]	[MAG]	[ANG.]
1	1.06	-124.08	17.25	108.49	0.047	29.03	0.58	-109.31
2	0.76	-138.82	9.88	102.48	0.053	33.75	0.30	-79.89
3	0.75	-172.59	6.66	83.80	0.055	24.66	0.30	-117.17
4	0.76	176.51	5.07	74.18	0.057	24.78	0.26	-132.68
5	0.69	156.74	4.37	62.96	0.064	22.70	0.27	-116.71
6	0.75	146.80	3.41	53.42	0.062	23.25	0.31	-136.56
7	0.75	136.66	2.98	44.49	0.066	22.61	0.28	-140.08
8	0.76	123.90	2.48	35.68	0.066	21.70	0.33	-142.73
9	0.80	118.44	2.14	28.41	0.068	22.21	0.33	-152.46
10	0.80	109.35	1.86	20.30	0.070	21.14	0.34	-154.38
11	0.84	102.78	1.60	14.42	0.071	21.44	0.37	-159.09
12	0.86	98.80	1.40	7.98	0.073	20.49	0.37	-163.49
13	0.87	96.34	1.23	3.13	0.075	20.85	0.40	-168.00
14	0.90	93.08	1.10	-2.14	0.078	20.07	0.43	-173.99
15	0.90	89.40	0.99	-7.19	0.080	19.07	0.44	-175.36
16	0.92	89.41	0.90	-10.53	0.084	19.32	0.47	177.53
17	0.91	85.22	0.83	-15.80	0.088	17.69	0.49	174.88
18	0.92	84.64	0.76	-18.17	0.093	18.98	0.52	174.58
19	0.89	84.59	0.71	-21.33	0.099	18.46	0.54	171.42
20	0.92	81.40	0.65	-24.94	0.103	17.38	0.56	170.14
21	0.91	79.59	0.62	-27.85	0.112	16.92	0.58	168.41
22	0.91	78.99	0.58	-31.60	0.121	14.79	0.59	162.26
23	0.92	70.05	0.55	-35.64	0.130	11.75	0.63	163.01
24	0.90	66.44	0.51	-39.84	0.133	7.54	0.63	159.53
25	0.92	61.74	0.47	-43.81	0.138	5.90	0.63	156.32
26	0.93	54.09	0.42	-46.68	0.140	4.57	0.65	155.80

#### S-PARAMETERS (Vds = 28V, Ids = 60mA, T = 25°C)

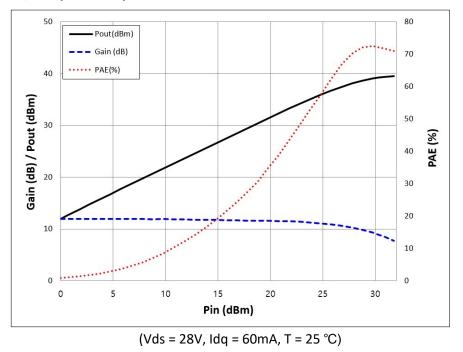
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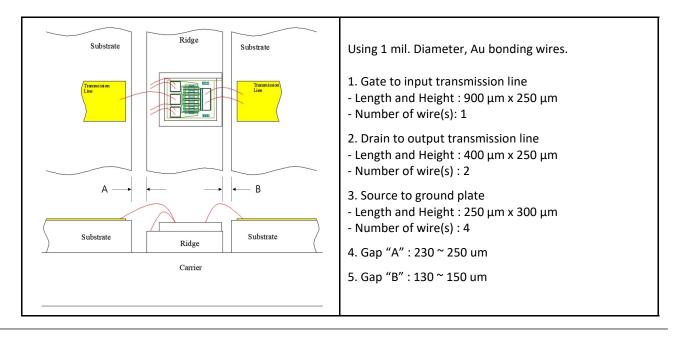


PIN\_POUT / Gain, PAE (@ 12 GHz)



#### WIRE BONDING INFORMATION

Always follow wire bonding diagrams recommended by BeRex for each device to achieve optimum device performance and reliability. As a general rule, bonding temperature should be kept to a maximum of 280°C for no longer than 2 minutes for all bonding wires.





# **ESD** Rating

ESD Testing Mode	Reference (Current Revision)	Resulting Classification
ESD - HBM	JDS - 001 - 2017	Class1A (Passes <500V)



# HANDLING PRECAUTIONS

GaN HEMTs are very sensitive to and may be damaged by Electrostatic Discharge (ESD). Therefore, proper ESD precautions must be taken whenever you are handling these devices. It is critically important that all work surfaces, and assembly equipment, as well as the operator be properly grounded when handling these devices to prevent ESD damage.

#### **DIE ATTACH RECOMMENDATIONS**

BeRex recommends the "Eutectic" die attach using Au/Sn (80/20) pre-forms. The die attach station must have accurate temperature control, and the operation should be performed with parts no hotter than 300°C for less than 10 seconds. An inert forming gas (90%  $N_2/10\%$   $H_2$ ) or clean, dry  $N_2$  should be used.

Use of conductive epoxy (gold or silver filled) may also be acceptable for die-attaching low power devices.

#### **SHIPPING & STORAGE**

BeRex's standard chip device shipping package consists of an antistatic "Gel-Pak", holding the chips, placed inside a sealed metallized bag. This packaging is designed to provide a reasonable measure of protection from both mechanical and ESD damage.

Chip devices should be stored in a clean, dry Nitrogen gas environment at room temperature until they are required for assembly. Only open the shipping package or perform die assembly in a work area with a class 10,000 or better clean room environment to prevent contamination of the exposed devices.

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# CAUTION

THIS PRODUCT CONTAINS GALLIUM NITRIDE (GaN) WHICH CAN BE HAZARDOUS TO THE HUMAN BODY AND THE ENVIRONMENT. THEREFORE, IT MUST BE HANDLED WITH CARE AND IN ACCORDANCE WITH ALL GOVERNMENTAL AND COMPANY REGULATIONS FOR THE SAFE HANDLING AND DISPOSAL OF HAZARDOUS WASTE. DO NOT BURN, DESTROY, CUT, CRUSH OR CHEMICALLY DISSOLVE THE PRODUCT. DO NOT LICK THE PRODUCT OR IN ANY WAY AL-LOW IT TO ENTER THE MOUTH. EXCLUDE THE PRODUCT FROM GENERAL INDUSTRIAL WASTE OR GARBAGE AND DISPOSE OF ONLY IN ACCORDANCE TO APPLICABLE LAWS AND/OR ORDINANCES.

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